

3-10-05

IFC



Docket No. 60893 (70840)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application: Nakai

Examiner: Richards, N. Drew

Application No.: 10/782,731

Art Unit: 2815

Filed: February 18, 2004

Confirmation: 3390

For: Semiconductor apparatus and method for fabricating the same

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By: _____

George W. Neuner

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE TO RESTRICTION REQUIREMENT

In the Office Action dated February 7, 2005, claims 1-18 are pending.
Restriction is required to one of the following inventions:

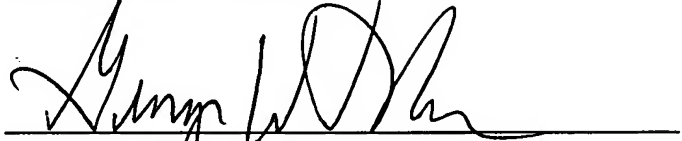
- I. Claims 1-11, drawn to a device.
- II. Claims 16-18, drawn to a method.

Nakai
Application No. 10/782,731
Page 2 of 2

Applicants hereby elect the invention of Group I, claims 1-11, without traverse for further examination in the present application.

An early examination and notice of allowance are earnestly solicited.

Respectfully submitted,


George W. Neuner (Reg. No. 26,964)

Date: March 7, 2005

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BOS2_480551.1